

PATENT APPLICATION SERIAL NO. \_\_\_\_\_

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE  
FEE RECORD SHEET

10/22/2003 LWONDIM1 00000001 10687877

01 FC:1001

770.00 DP

Adjustment date: 12/11/2003 EEKUBAY1  
10/22/2003 LWONDIM1 00000001 10687877  
01 FC:1001 -770.00 DP

12/11/2003 EEKUBAY1 00000016 10687877

01 FC:2001

385.00 DP

Repln. Ref: 12/11/2003 EEKUBAY1 0008545800  
DAH:022448 Name/Number:10687877  
FC: 9204 \$385.00 CR

PTO-1556  
(5/87)



DOF/RLT  
#387  
PATENT OFFICE  
MS 16  
PATENT  
2450-0570P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: HWANG, Jyn-Guo et al. Conf.:  
Appl. No.: 10/687,877 Group:  
Filed: October 20, 2003 Examiner:  
For: CMOS IMAGE SENSOR SINGLE CHIP INTEGRATED  
WITH A RF TRANSMITTER  
ATTENTION: Refund Section  
Accounting Division  
OFFICE OF FINANCE

REQUEST FOR A REFUND OF 50% OF THE FILING FEE

MS 16  
Director of the U.S. Patent  
and Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 3, 2003

Sir:

The following documentation and remarks are respectfully  
submitted in connection with the above-identified application.

DOCUMENTATION

Attached hereto is:

- ☒ a copy of written assertion of small entity status  
filed on even date herewith under separate cover; and/or  
☐ a copy of an executed Statement Claiming Small Entity  
Status (37 C.F.R. § 1.9(f) and 1.27( )) - filed on even  
date herewith under separate cover.

RECEIVED  
DEC 10 2003  
OFFICE OF FINANCE  
ACCOUNTING DIVISION  
REFUND BRANCH

PATENT  
OFFICE

ADVICE

REMARKS

Pursuant to 37 C.F.R. § 1.28(a), applicants hereby request a refund of 50% of the Filing Fee. Small entity status has been established by the filing of a written assertion/statement on December 3, 2003.

Small entity applicants may now file the necessary Statement within three (3) months of the date of payment of any particular fee and request a refund. The present application qualifies for the reduced fees as implemented by the U.S. Patent and Trademark Office on October 1, 1982, and further amended on January 20, 1983 and September 8, 2000.

The present application was filed on October 20, 2003 with a filing fee of \$770.00. It is respectfully requested that the excess fee of \$385.00 be credited to Deposit Account No. 02-2448.

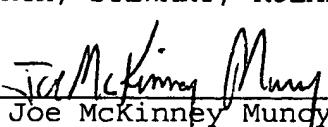
Favorable action on the present Request is respectfully requested.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By

  
Joe McKinney Mundy, #32,334

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Falls Church, VA 22040-0747  
(703) 205-8000

KM/msh  
2450-0570P  
Attachment



PATENT  
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OFFICE

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: HWANG, Jyn-Guo et al. Conf.:  
Appl. No.: 10/687,877 Group:  
Filed: October 20, 2003 Examiner:  
For: CMOS IMAGE SENSOR SINGLE CHIP INTEGRATED  
WITH A RF TRANSMITTER

ASSERTION OF SMALL ENTITY STATUS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 3, 2003

Sir:

The above-identified application qualifies for small entity status. This written assertion of small entity status should satisfy the requirements of 37 C.F.R. § 1.27.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By Joe McKinney Muncy  
Joe McKinney Muncy, #32,334

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Attachment(s)

(Rev. 09/30/03)